



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN CPU)

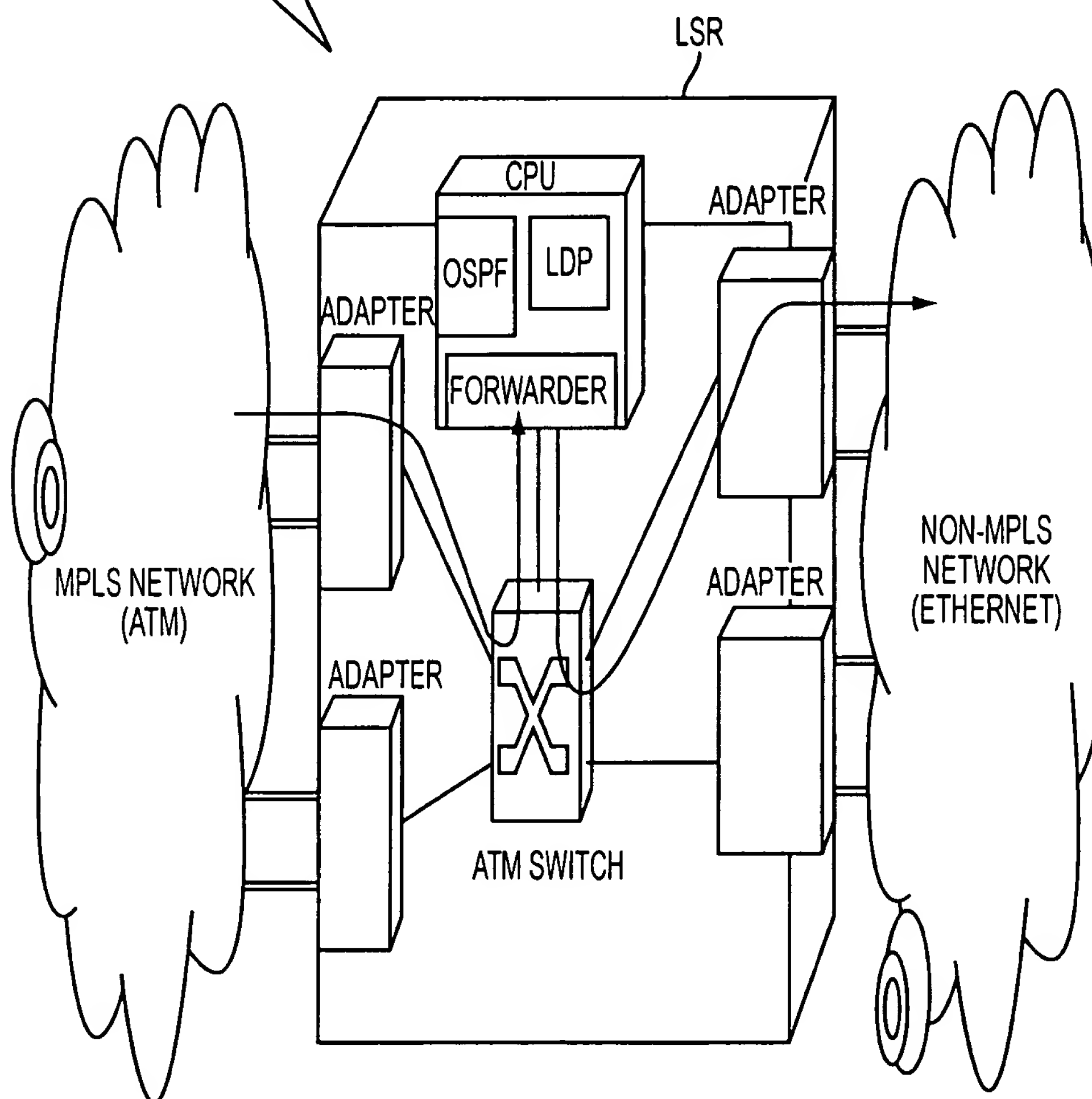


FIG. 1
PRIOR ART

EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED AT PRE-STAGE
OF CPU AND SHORT-CUTS CPU)

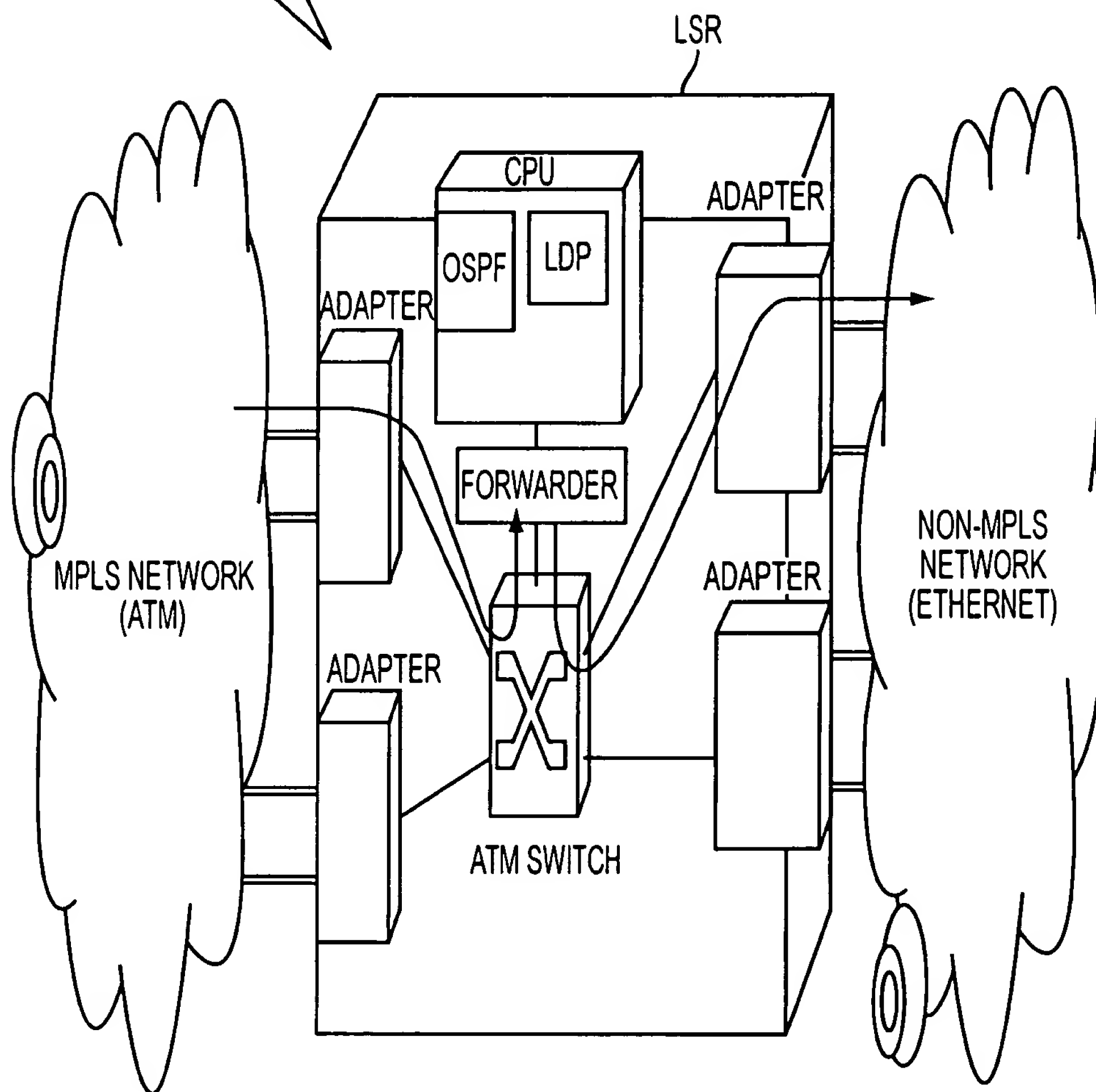
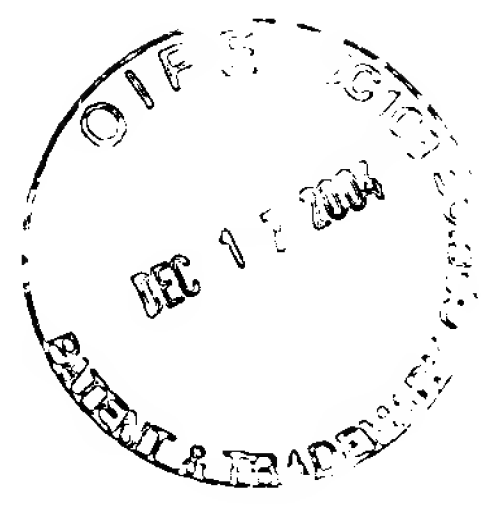


FIG. 2
PRIOR ART



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN EACH ADAPTER)

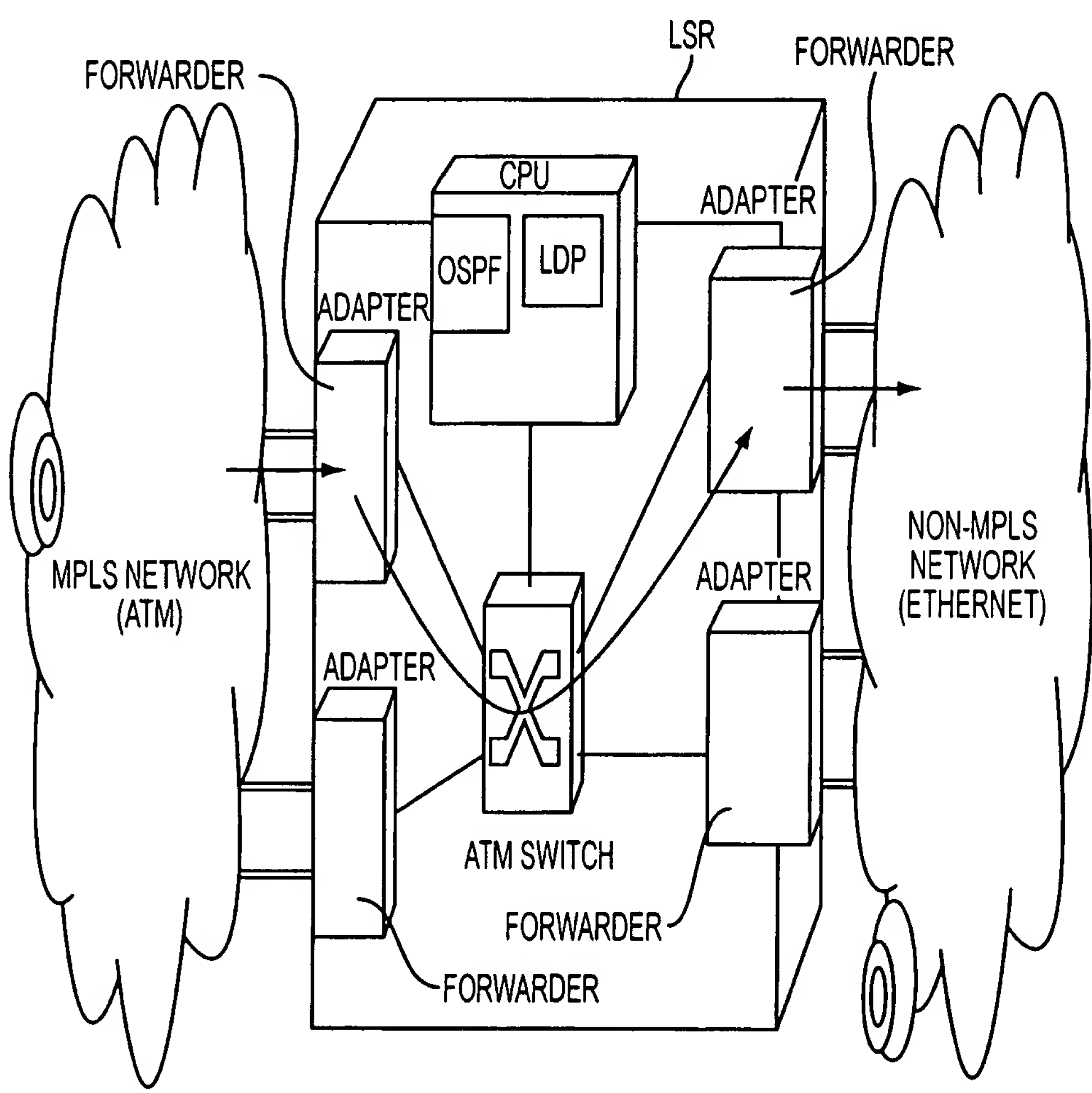


FIG. 3
PRIOR ART



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN ADAPTER
AT EGRESS FOR PACKETS)

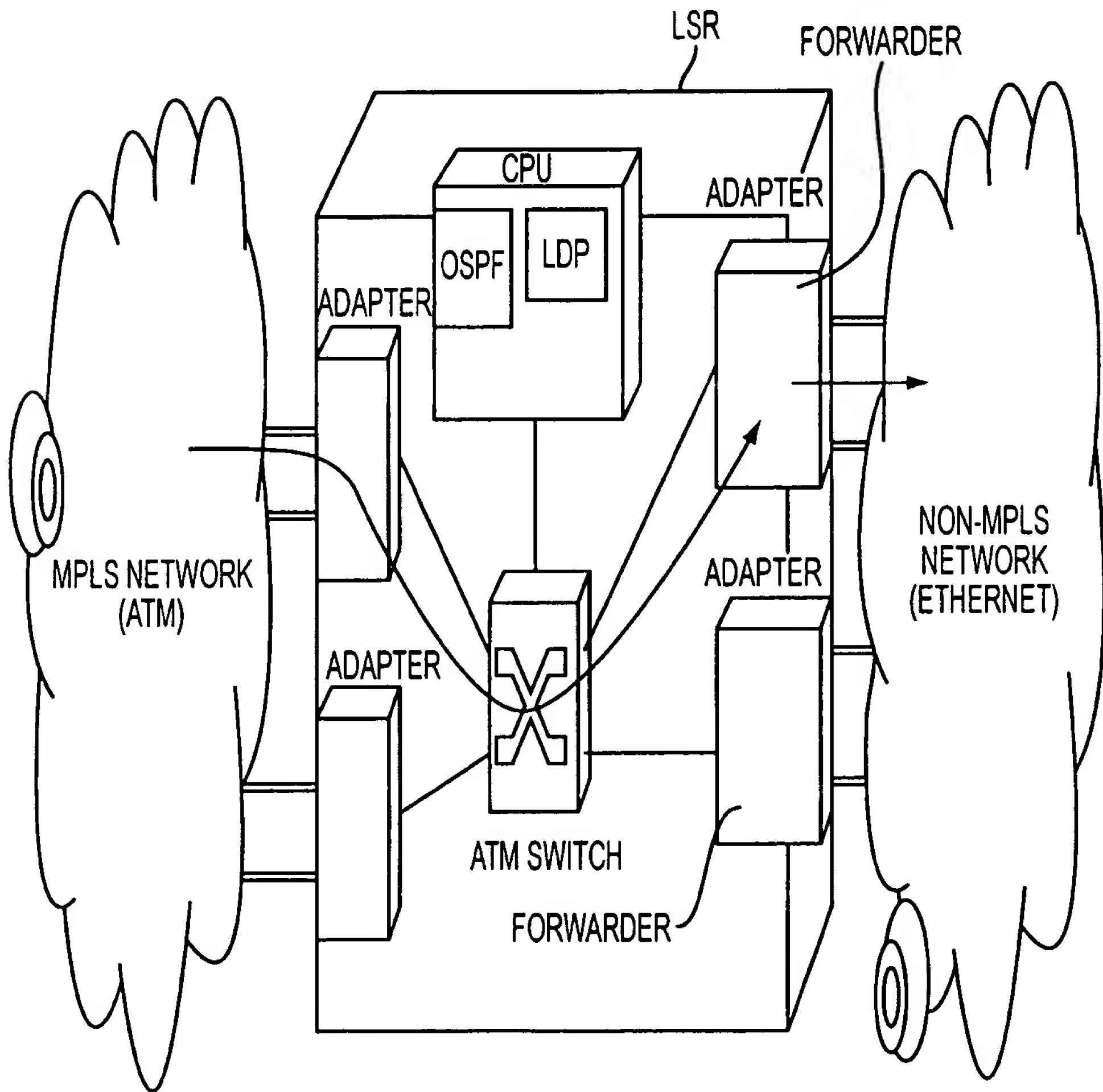


FIG. 4

PRIOR ART

Application of the Invention

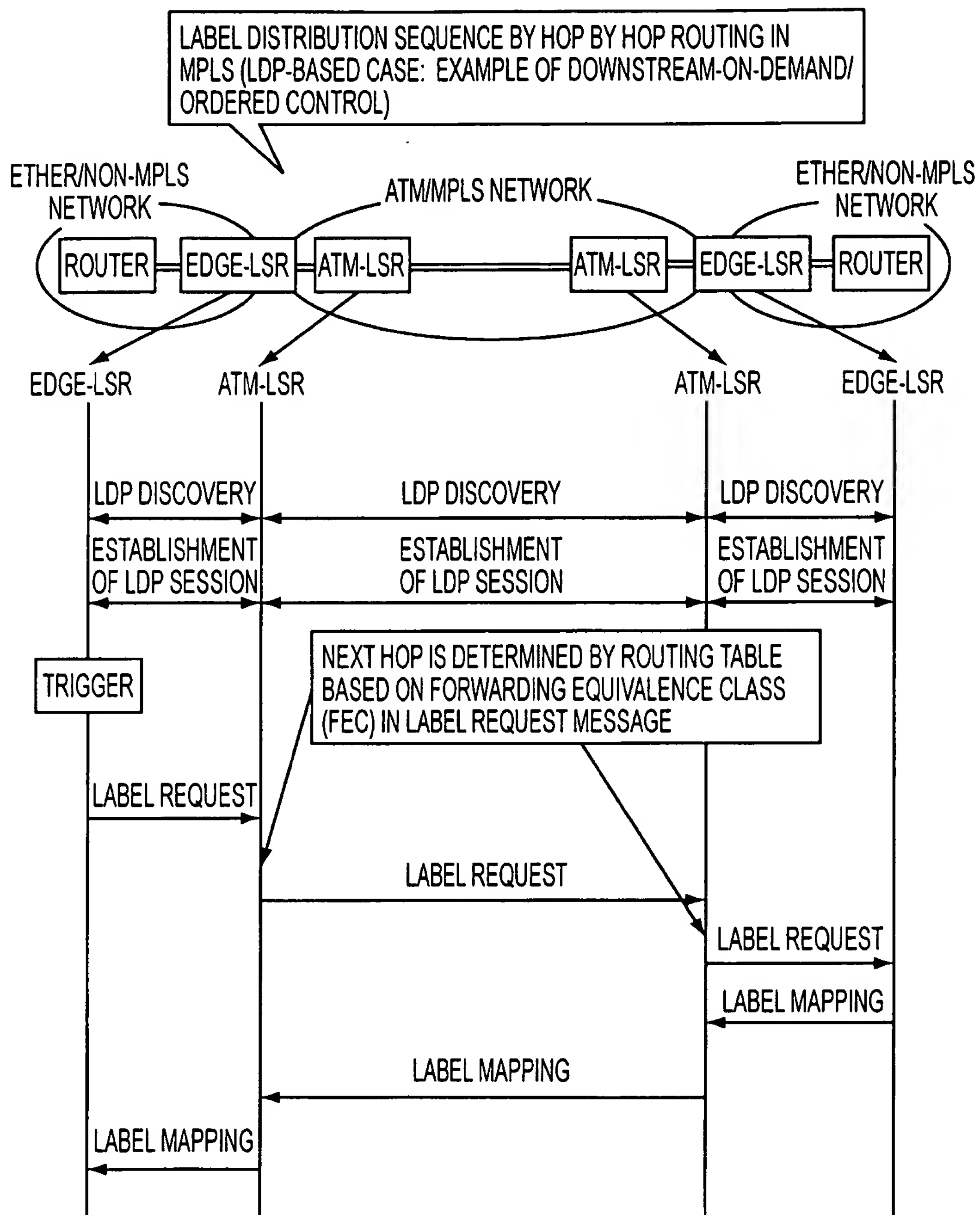


FIG. 5
PRIOR ART

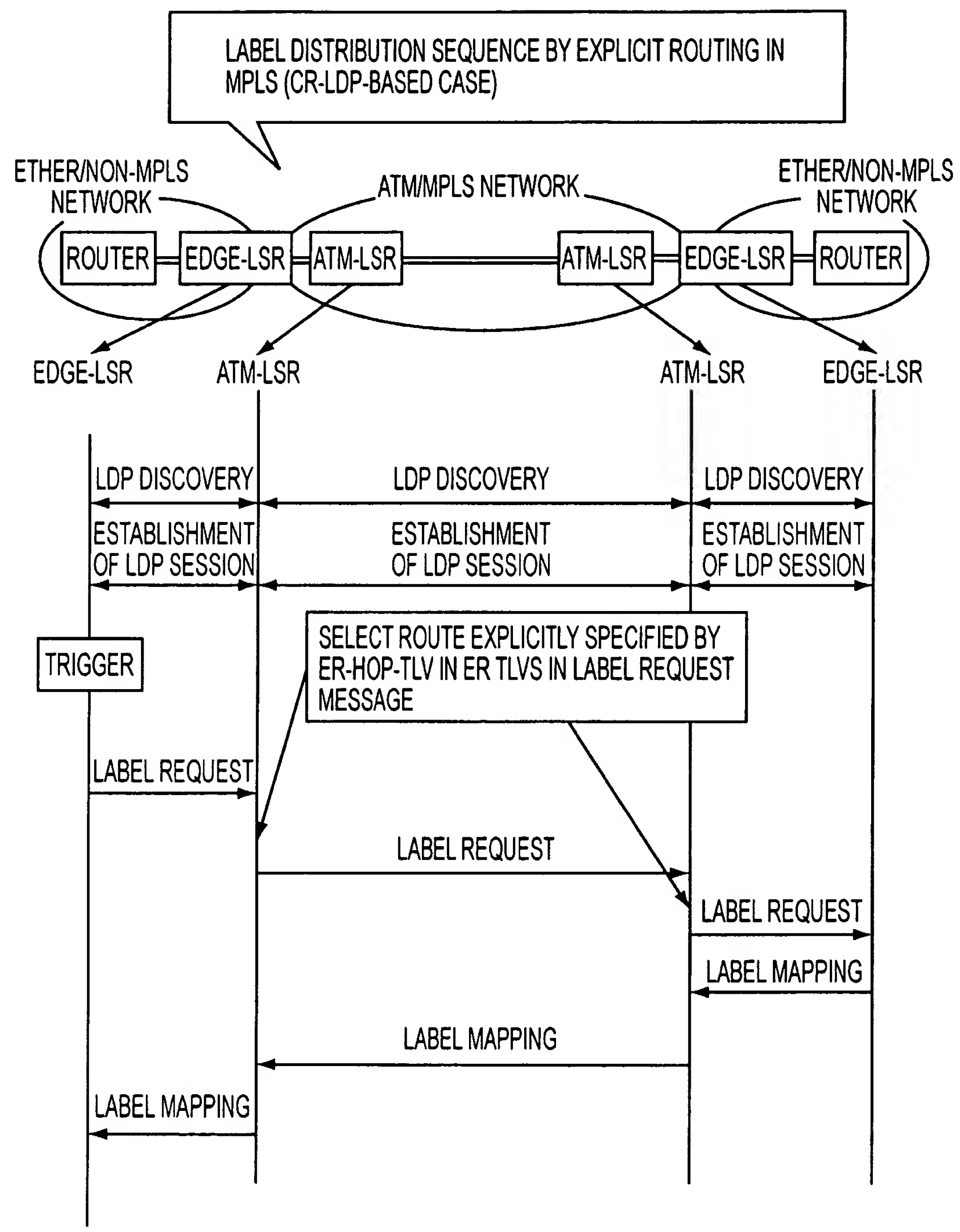


FIG. 6
PRIOR ART

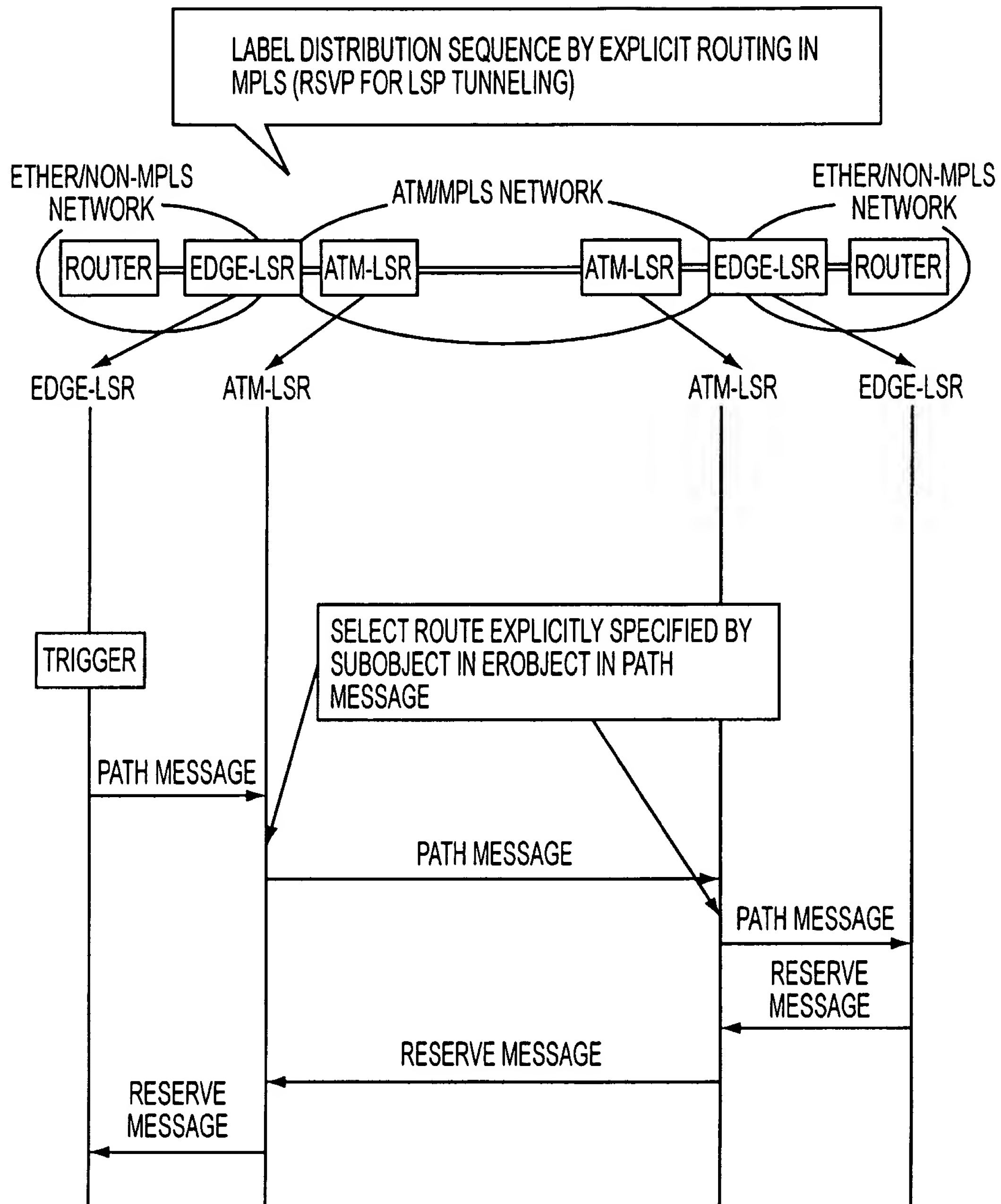


FIG. 7
PRIOR ART

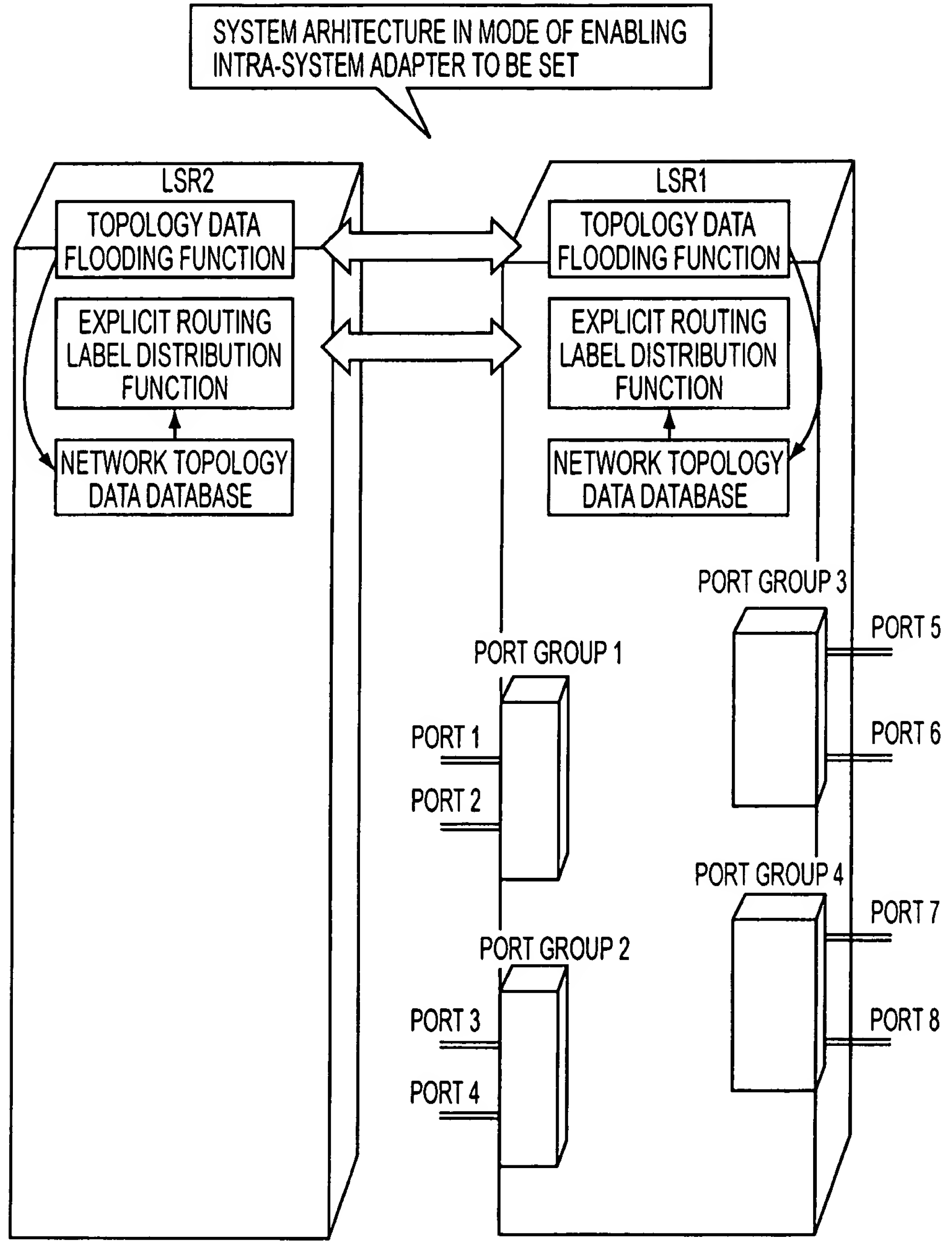


FIG. 11

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FIG. 25